


Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	93487	(peak or maximi\$6) same (buffer\$3 or storage or storing or RAM or ROM or memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/18 17:34
L2	339	(re-generat\$3 or regenerat\$3) same (FFT or IFFT or DFT or IDFT or (Fourier near5 transform\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/18 17:35
L3	28	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/18 17:34
L4	47091	(re-generat\$3 or regenerat\$3) with (data or symbol or bit or signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/18 17:35
L5	3810	1 and (FFT or IFFT or DFT or IDFT or (Fourier near5 transform\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/18 17:36
L6	133	4 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/18 17:36

Day : Monday
Date: 7/18/2005


PALM INTRANET

Time: 15:42:26

Inventor Name Search Result

Your Search was:

Last Name = TAUNTON

First Name = MARK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
60578898	Not Issued	159	06/14/2004	INSTRUCTION FOR VITERBI CODING	TAUNTON, MARK
60507522	Not Issued	159	10/02/2003	PROCESSOR EXECUTION UNIT FOR COMPLEX OPERATIONS	TAUNTON, MARK
60506732	Not Issued	159	09/30/2003	SIMD INSTRUCTION FOR REAL/COMPLEX FFT CONVERSION	TAUNTON, MARK
60506727	Not Issued	159	09/30/2003	SIMD INSTRUCTIONS FOR COMPLEX MULTIPLY-ACCUMULATE	TAUNTON, MARK
60506487	Not Issued	159	09/29/2003	SIMD INSTRUCTION FOR FLEXIBLE FFT BUTTERFLY	TAUNTON, MARK
60505860	Not Issued	159	09/26/2003	SYSTEM AND METHOD FOR DESCramBLING AND BIT-CORRECTING PAYLOAD BYTES IN AN ASYNCHRONOUS TRANSFER MODE CELL	TAUNTON, MARK
60505859	Not Issued	159	09/26/2003	SYSTEM AND METHOD FOR DESCramBLING DIGITAL SUBSCRIBER LINE DATA	TAUNTON, MARK
60505858	Not Issued	159	09/26/2003	SYSTEM AND METHOD FOR GENERATING HEADER ERROR CONTROL BYTE FOR ASYNCHRONOUS TRANSFER MODE CELL	TAUNTON, MARK
60505857	Not Issued	159	09/26/2003	SYSTEM AND METHOD FOR BIT REVERSING AND SCRAMBLING PAYLOAD BYTES IN AN ASYNCHRONOUS TRANSFER MODE CELL	TAUNTON, MARK

<u>60505846</u>	Not Issued	159	09/26/2003	SYSTEM AND METHOD FOR SCRAMBLING DIGITAL SUBSCRIBER LINE DATA	TAUNTON, MARK
<u>60505721</u>	Not Issued	159	09/25/2003	SIMD INSTRUCTION FOR DMT ENCODING	TAUNTON, MARK
<u>11003398</u>	Not Issued	020	12/06/2004	REGISTER ADDRESSING	TAUNTON, MARK
<u>10956091</u>	Not Issued	020	10/04/2004	PROCESSOR EXECUTION UNIT FOR COMPLEX OPERATIONS	TAUNTON, MARK
<u>10953584</u>	Not Issued	030	09/30/2004	METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT FOR EXECUTING SIMD INSTRUCTION FOR REAL/COMPLEX FFT CONVERSION	TAUNTON, MARK
<u>10953421</u>	Not Issued	030	09/30/2004	METHODS FOR PERFORMING MULTIPLY-ACCUMULATE OPERATIONS ON OPERANDS REPRESENTING COMPLEX NUMBERS	TAUNTON, MARK
<u>10952169</u>	Not Issued	030	09/29/2004	METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT FOR EXECUTING SIMD INSTRUCTION FOR FLEXIBLE FFT BUTTERFLY	TAUNTON, MARK
<u>10951867</u>	Not Issued	030	09/29/2004	METHODS FOR PERFORMING MULTIPLICATION OPERATIONS ON OPERANDS REPRESENTING COMPLEX NUMBERS	TAUNTON, MARK
<u>10949517</u>	Not Issued	020	09/27/2004	DSL TRELLIS ENCODING	TAUNTON, MARK
<u>10949466</u>	Not Issued	030	09/27/2004	PROCESSOR INSTRUCTION FOR DMT ENCODING	TAUNTON, MARK
<u>10946306</u>	Not Issued	020	09/22/2004	DATA DE-SCRAMBLER	TAUNTON, MARK
<u>10946305</u>	Not Issued	030	09/22/2004	SYSTEM AND METHOD FOR BIT-REVERSING AND SCRAMBLING PAYLOAD BYTES IN AN ASYNCHRONOUS TRANSFER MODE CELL	TAUNTON, MARK
<u>10946304</u>	Not Issued	030	09/22/2004	SYSTEM AND METHOD FOR GENERATING HEADER ERROR CONTROL BYTE FOR	TAUNTON, MARK

				ASYNCHRONOUS TRANSFER MODE CELL	
<u>10946303</u>	Not Issued	030	09/22/2004	SYSTEM AND METHOD FOR DE-SCRAMBLING AND BIT- ORDER-REVERSING PAYLOAD BYTES IN AN ASYNCHRONOUS TRANSFER MODE CELL	TAUNTON, MARK
<u>10946173</u>	Not Issued	020	09/22/2004	DATA SCRAMBLER	TAUNTON, MARK
<u>10607169</u>	Not Issued	030	06/27/2003	BURST MODE MEMORY FETCHES WHEN DECODING COMPRESSED IMAGE DATA	TAUNTON, MARK
<u>10464544</u>	<u>6836837</u>	150	06/19/2003	REGISTER ADDRESSING	TAUNTON, MARK
<u>10431611</u>	<u>6754392</u>	150	05/08/2003	SWITCHING BETWEEN DECODED IMAGE CHANNELS	TAUNTON, MARK
<u>09921758</u>	Not Issued	030	08/06/2001	MULTI-TONE TRANSMISSION	TAUNTON, MARK
<u>09921757</u>	Not Issued	061	08/06/2001	MULTI-TONE TRANSMISSION	TAUNTON, MARK
<u>09921756</u>	Not Issued	030	08/06/2001	MULTI-TONE TRANSMISSION	TAUNTON, MARK
<u>09703837</u>	<u>6601157</u>	150	11/01/2000	REGISTER ADDRESSING	TAUNTON, MARK
<u>09651799</u>	<u>6404357</u>	150	08/30/2000	DIGITAL/ANALOGUE COMMUNICATION SYSTEM FOR INCREASING TRANSFER EFFICIENCY OF DIGITAL SAMPLE DATA	TAUNTON, MARK
<u>09438361</u>	<u>6618440</u>	150	11/12/1999	BURST MODE MEMORY FETCHES WHEN DECODING COMPRESSED IMAGE DATA	TAUNTON, MARK
<u>09273722</u>	<u>6591013</u>	150	03/22/1999	SWITCHING BETWEEN DECODED IMAGE CHANNELS	TAUNTON, MARK
<u>09212567</u>	Not Issued	161	12/16/1998	DECODING PACKETS OF DIGITAL DATA	TAUNTON, MARK

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
TAUNTON	MARK	Search